

09816338_CLS

Most Frequently Occurring Classifications of Patents Returned
From A Search of 09816338 on March 04, 2004

Original Classifications

3 712/241
2 710/52
2 712/234
2 712/243
2 712/245
2 714/21

Cross-Reference Classifications

2 712/248

Combined Classifications

3 712/234
3 712/241
3 712/245
3 712/248
2 700/12
2 710/5
2 710/52
2 712/216
2 712/226
2 712/233
2 712/243
2 714/21

3
2
7
141

09816338_CLSTITLES

Titles of Most Frequently Occurring Classifications of Patents Returned

From A Search of 09816338 on March 04, 2004

3	712/234	(2 OR, 1 XR)
	Class 712 :	ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: PROCESSING ARCHITECTURES AND INS
TRUCTION		
		PROCESSING
	712/220	PROCESSING CONTROL
	712/233	.Branching (e.g., delayed branch, loop control
		branch predict, interrupt)
	712/234	..Conditional branching
3	712/241	(3 OR, 0 XR)
	Class 712 :	ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: PROCESSING ARCHITECTURES AND INS
TRUCTION		
		PROCESSING
	712/220	PROCESSING CONTROL
	712/233	.Branching (e.g., delayed branch, loop control
		branch predict, interrupt)
	712/241	..Loop execution
3	712/245	(2 OR, 1 XR)
	Class 712 :	ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: PROCESSING ARCHITECTURES AND INS
TRUCTION		
		PROCESSING
	712/220	PROCESSING CONTROL
	712/245	.Processing sequence control (i.e., microsequencing)
3	712/248	(1 OR, 2 XR)
	Class 712 :	ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: PROCESSING ARCHITECTURES AND INS
TRUCTION		
		PROCESSING
	712/220	PROCESSING CONTROL
	712/245	.Processing sequence control (i.e., microsequencing)
	712/248	..Writable/changeable control store architecture
2	700/12	(1 OR, 1 XR)

09816338 CLSTITLES

Class 700 : DATA PROCESSING: GENERIC CONTROL SYSTEMS OR
SPECIFIC APPLICATIONS

700/1 GENERIC CONTROL SYSTEM, APPARATUS OR PROCESS

700/11 .Sequential or selective

700/12 ..State of condition or parameter (e.g.,
on/off)

2 710/5 (1 OR, 1 XR)

Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT

710/1 INPUT/OUTPUT DATA PROCESSING

710/5 .Input/Output command process

2 710/52 (2 OR, 0 XR)

Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT

710/1 INPUT/OUTPUT DATA PROCESSING

710/52 .Input/Output data buffering

2 712/216 (1 OR, 1 XR)

Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: PROCESSING ARCHITECTURES AND INS

TRUCTION

712/216 PROCESSING
DYNAMIC INSTRUCTION DEPENDENCY CHECKING,
MONITORING OR CONFLICT RESOLUTION

2 712/226 (1 OR, 1 XR)

Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: PROCESSING ARCHITECTURES AND INS

TRUCTION

712/220 PROCESSING

712/226 PROCESSING CONTROL

712/226 .Instruction modification based on condition

2 712/233 (1 OR, 1 XR)

Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: PROCESSING ARCHITECTURES AND INS

TRUCTION

712/220 PROCESSING

712/233 PROCESSING CONTROL

712/233 .Branching (e.g., delayed branch, loop control
branch predict, interrupt)

2 712/243 (2 OR, 0 XR)

Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: PROCESSING ARCHITECTURES AND INS

09816338_CLSTITLES

TRUCTION

PROCESSING

712/220 PROCESSING CONTROL

712/233 .Branching (e.g., delayed branch, loop control

branch predict, interrupt)

712/243 ..To microinstruction subroutine

2 714/21 (2 OR, 0 XR)

Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY

714/100 DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING

714/1 .Reliability and availability

714/2 ..Fault recovery

714/15 ...State recovery (i.e., process or data file)

714/21State validity check

09816338 LIST

PLUS Search Results for S/N 09816338, Searched March 04, 2004

The Patent Linguistics Utility System (PLUS) is a USPTO automated search system for U.S. Patents from 1971 to the present. PLUS is a query-by-example search system which produces a list of patents that are most closely related linguistically to the application searched. This search was prepared by the staff of the Scientific and Technical Information Center, SIRA.

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4984151 99
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